

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): HARADA et al.	Atty. Dkt.: 01-628
Serial No.: Unknown	Group Art Unit:
Filed: Concurrently herewith	Examiner:
Title: MULTI-LAYER PRINTED CIRCUIT BOARD AND METHOD FOR MANUFACTURING THE SAME	

Commissioner for Patents  
Arlington, VA 22202

Date: April 1, 2004

**INFORMATION DISCLOSURE STATEMENT**

Sir:

Pursuant to 37 C.F.R. §1.56, the reference(s) listed on the attached Form PTO-1449 is/are submitted for consideration by the Examiner without any admission that it/they constitute(s) statutory prior art, or without any admission that it/they contain(s) subject matter that anticipates the invention or renders the invention obvious to a person of ordinary skill in the art.

The Examiner is requested to initial the attached PTO Form-1449 and to return a copy of same to the undersigned attorney as proof that the listed reference(s) has/have been considered and made of record.

Respectfully submitted,



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FORM PTO-1449	ATTY. DKT NO.	01-628	SER. NO.
	APPLICANT	HARADA et al.	
	FILING DATE	April 1, 2004	GROUP

**REFERENCE DESIGNATION**

**U.S. PATENT DOCUMENTS**

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS
		4,931,354 (Discussed in page 1 of the spec.)	Jun. 5, 1990	Wakino et al.		
		6,228,467	May 8, 2001	Taniguchi et al.		

**FOREIGN PATENT DOCUMENTS**

**TRANSLATION**

		DOCUMENT NUMBER	DATE	COUNTRY	NAME	CLASS	SUB CLASS	YES	NO	
										Eng. Abstract
		JP-A-H07-307575 (Discussed in pages 1-2 of the spec.)	11/21/95	Japan						X

\* Full English text is available in machine-translated form in JPO (Japanese Patent Office) English language web site at <http://www1.ipdl.jpo.go.jp/PA1/cgi-bin/PA1INDEX>.

**OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)**

EXAMINER	DATE CONSIDERED	